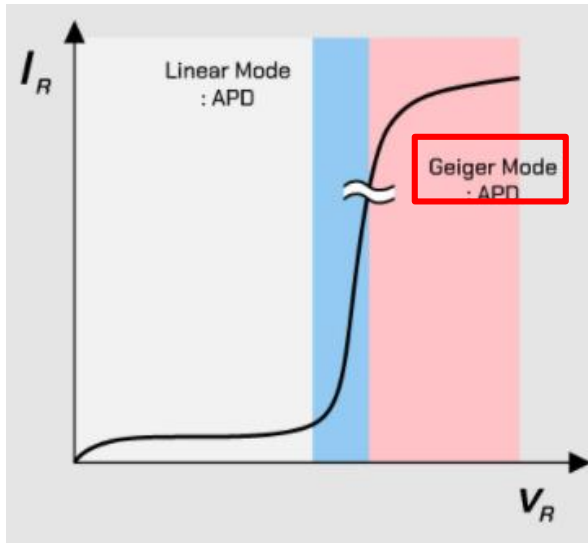


# 2021-1~2022-1 Works

## SPAD



Operation mode	Reverse voltage	Gain
Linear Mode	Below breakdown voltage	Dozens to several hundred
Geiger Mode	Above breakdown voltage	$10^5$ to $10^6$

## SPAD Design

- GF 40nm process
- DB 110nm CIS process
- GF 55nm BCD process

## TCAD Simulation

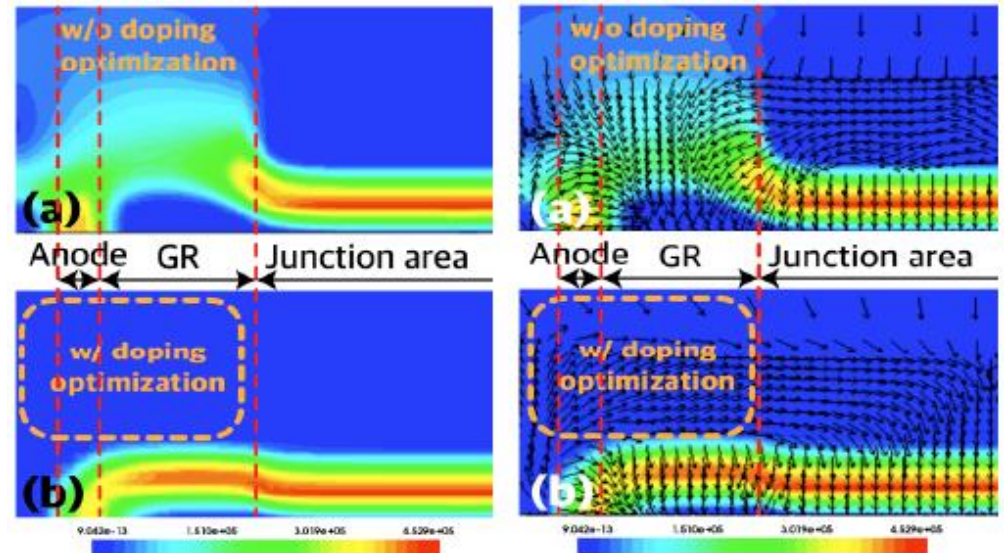


Fig. 3. TCAD simulation results for the back-illuminated SPAD: E-field profiles (a) w/o and (b) w/ doping optimization at  $V_E = 2.5$  V.

Fig. 4. TCAD simulation results for the back-illuminated SPAD: Carrier flows (a) w/o and (b) w/ doping optimization.

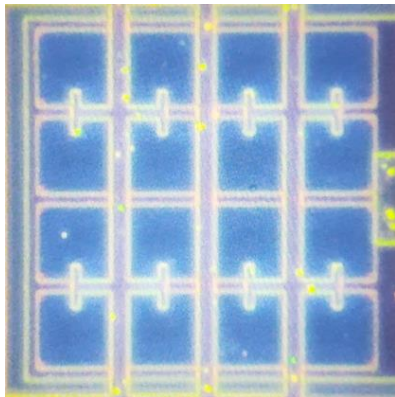
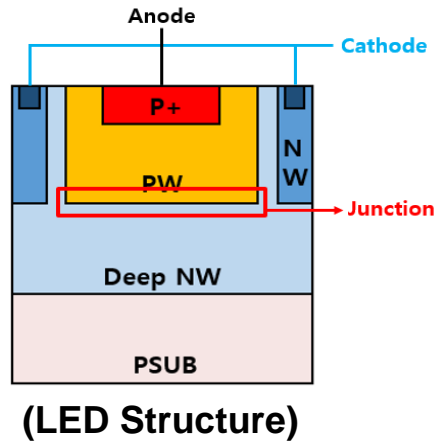
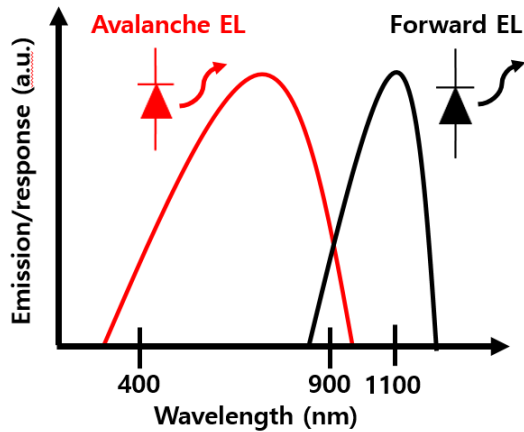
Doping-Optimized Back-Illuminated Single-Photon Avalanche Diode in Stacked 40 nm CIS Technology Achieving 60% PDP at 905 nm

Eunsung Park<sup>1,2</sup>, Won-Yong Ha<sup>1</sup>, **Dovoon Eom<sup>1\*</sup>**, Dae-Hwan Ahn<sup>1</sup>, Hyuk An<sup>3</sup>, Suhyun Yi<sup>3</sup>, Kyung-Do Kim<sup>3</sup>, Jongchae Kim<sup>2</sup>, Woo-Young Choi<sup>2\*</sup>, and Myung-Jae Lee<sup>1,4</sup>

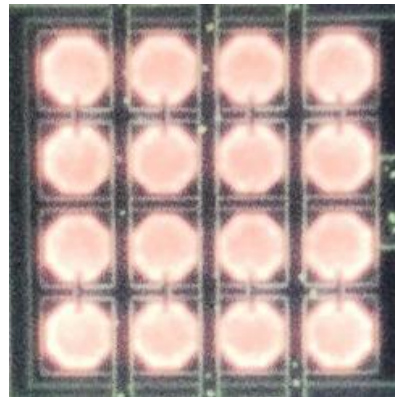
(2022 IEDM co-author)

# 2021-1~2022-1 Works

## AMLED (Avalanche Mode LED)



(Below  $V_{br}$ )



(Above  $V_{br}$ )

## Conference

### Avalanche Mode LED Based on CMOS Technology

Doyoon Eom<sup>1</sup>, Woo-Young Choi<sup>2</sup>, and Ming-Jae Lee<sup>2\*</sup>  
<sup>1</sup>Department of Electrical and Electronic Engineering, Yonsei University, Korea  
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A light-emitting diode(LED) is widely used in many applications like display, communications, biomedical applications, and general illumination. Among them, research on optical coupling is being actively progressed with a lot of interest. In particular, monolithic optical coupling based on CMOS technology has been reported recently[1]. For monolithic optical coupling, Si based LEDs are essential. In general, Si LEDs operate in forward bias and emit light in the near infrared(NIR) wavelength, whereas CMOS photodiode(PDs) have low detection efficiency in this wavelength range (Fig. 1(a)). Therefore, the optical coupling performance is poor. Due to this, Si-V LEDs are usually used, but the fabrication of Si-V LED is complex and expensive compared to Si LEDs. If avalanche-mode LEDs based on Si are used, it is possible to shift the LED wavelength from NIR to VIS, and consequently, better optical coupling efficiency can be achieved (Fig. 1(b)). In this paper, the avalanche-mode LEDs fabricated in the CMOS technology is demonstrated and their characteristics are reported.

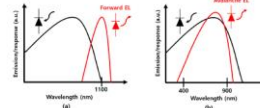


Fig. 1. (a) Forward electroluminescence of Si LED, (b) Avalanche electroluminescence of Si LED

Acknowledgement: The authors acknowledge the financial support from the Korea Institute of Science and Technology(KIST) Intronics Program(Grant No. 2E1911).

References: [1] D. Eom, S. "Avalanche-mode silicon LEDs for monolithic optical coupling in CMOS technology," Ph.D. dissertation, University of Texas, The Netherlands, 2017.

### CMOS 기반 Avalanche-Mode LED의 효율 특성 연구

임도윤<sup>1</sup>, 최우영<sup>2</sup>, 최우영<sup>2</sup>, 이명재<sup>2\*</sup>  
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<sup>2</sup>한국과학기술연구원 광전자연구부 (KIST)  
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Light-emitting diode(LED)는 일반 조명, 디스플레이 그리고 통신용 등에 널리 사용되는 소자이다. 많은 응용에 있어 LED는 가시광선 영역의 빛을 방출해야 하기 때문에, LED는 일반적으로 근-UV 반도체 물질을 기반으로 제작된다. 하지만 근-UV 물질은 복잡한 구조를 가지고 비싸고 광 대역폭 범용성이 떨어진다. 반면에 근-IR CMOS 기술을 활용 시 가시광 LED를 생산할 수 있음은 소자간 비용 효율적인 연산이 가능하므로 CMOS 기술이 가시광에 monolithic 칩 제조의 핵심이 될 수 있다. 일반적으로 Si 기반 LED는 900~1000nm 파장대의 빛을 방출하기 때문에(Forward-Mode) 근-UV LED를 대체할 수 없다. 하지만 avalanche 영역에서 Si LED를 동작시키면(Avalanche-Mode) 가시광선 영역의 빛을 방출하기 때문에 기존의 LED를 대체할 수 있는 가능성을 가진다(그림 1). 본 논문에서는 CMOS 공정을 이용하여 제작된 Si LED의 Electro Luminescence(EL) 특성을 통해 가시광선 영역에서의 동작 특성을 확인하였다. 또한 unit cell을 이용하여 전체 소자의 크기가 동일한 두 가지 다른 LED를 제작 후 비교를 통해 성능 향상을 증명하였다(그림 2).

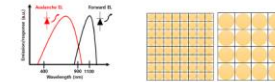


그림 1. Mode에 따른 방출 그래프, 그림 2. 두 가지 형태의 LED array

Acknowledgement: The authors acknowledge the financial support from the Korea Institute of Science and Technology(KIST) Intronics Program(Grant No. 2E1911).

References: [1] D. Eom, S. "Avalanche-mode silicon LEDs for monolithic optical coupling in CMOS technology," Ph.D. dissertation, University of Texas, The Netherlands, 2017.

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# Future Plan (2022-2)

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- **Masters degree graduation thesis**
  - [DB 110nm CIS] SPAD measurement
  - [DB 110nm CIS / GF 55nm BCD] AMLED measurement
  
- **TCAD Simulation**
  - SPAD parameter simulation
  
- **2022.08~ : GF 55nm BCD BSI (measurement / analysis)**